TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (U-MOSIII)

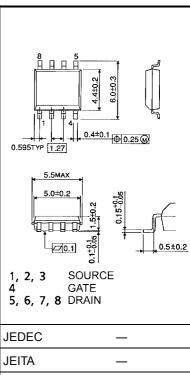
TPC8109

Lithium Ion Battery Applications Notebook PC Applications Portable Equipment Applications

- Small footprint due to small and thin package
- Low drain-source ON resistance: R_{DS} (ON) = 14 m Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 19 \text{ S} (typ.)$
- Low leakage current: $I_{DSS} = -10 \ \mu A \ (max) \ (V_{DS} = -30 \ V)$
- Enhancement-mode: $V_{th} = -0.8$ to -2.0 V ($V_{DS} = -10$ V, $I_D = -1$ mA)

Maximum Ratings (Ta = 25°C)

Characte	ristics	Symbol	Rating	Unit
Drain-source voltage		V _{DSS}	-30	V
Drain-gate voltage (F	k _{GS} = 20 kΩ)	V _{DGR}	-30	V
Gate-source voltage		V _{GSS}	±20	V
Drain current	DC (Note 1)	۱ _D	-10	А
Drain current	Pulse (Note 1)	I _{DP}	-40	~
Drain power dissipati	on (t = 10 s) (Note 2a)	PD	1.9	w
Drain power dissipati	on (t = 10 s) (Note 2b)	PD	1.0	W
Single pulse avalanche energy (Note 3)		E _{AS}	130	mJ
Avalanche current		I _{AR}	-10	А
Repetitive avalanche (energy Note 2a) (Note 4)	E _{AR}	0.19	mJ
Channel temperature	1	T _{ch}	150	°C
Storage temperature	range	T _{stg}	–55 to 150	°C

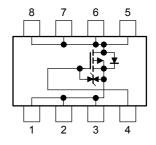


Weight: 0.080 g (typ.)

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Circuit Configuration

2-6J1B



Note: For (Note 1), (Note 2), (Note 3) and (Note 4), please refer to the next page.

This transistor is an electrostatic sensitive device. Please handle with caution.

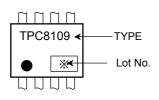
Unit: mm

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Thermal Characteristics

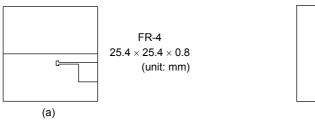
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient (t = 10 s) (Note 2a)	R _{th (ch-a)}	65.8	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	R _{th (ch-a)}	125	°C/W

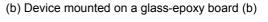
Marking (Note 5)

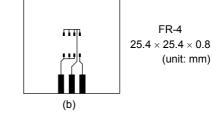


Note 1: Please use devices on condition that the channel temperature is below $150^{\circ}C$.

Note 2: (a) Device mounted on a glass-epoxy board (a)



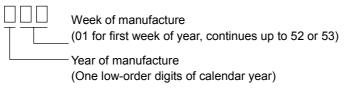




Note 3: $V_{DD} = -24 V$, $T_{ch} = 25^{\circ}C$ (initial), L = 1.0 mH, $R_G = 25 \Omega$, $I_{AR} = -10 \text{ A}$

Note 4: Repetitive rating; pulse width limited by maximum channel temperature

- Note 5: on lower left of the marking indicates Pin 1.
 - ※ Weekly code: (Three digits)



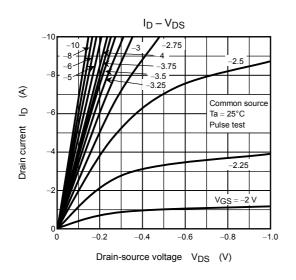
Electrical Characteristics (Ta = 25°C)

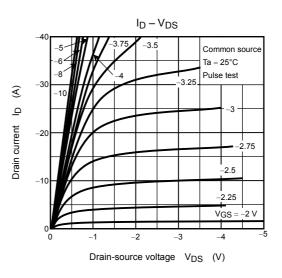
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage cur	rrent	I _{GSS}	$V_{GS}=\pm 16~V,~V_{DS}=0~V$	_		±10	μA	
Drain cut-OFF cu	irrent	I _{DSS}	$V_{DS} = -30$ V, $V_{GS} = 0$ V	_		-10	μA	
Drain gourge bro	akdown voltogo	V (BR) DSS	$I_{D} = -10$ mA, $V_{GS} = 0$ V	-30		_	V	
Diam-source bie	akuown vollage	V (BR) DSX	$I_D = -10$ mA, $V_{GS} = 20$ V	± 10 -10 -30 -15 -15 -0.8 -0.8 -0.8 -10 -0.8 -15 -0.8 -15 -0.8 -0.8 -0.8 -0.8 -15 -0.8 -15 -0.8 9 19 2260 350 5 5 34 143 143	v			
Gate threshold vo	oltage	V _{th}	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -1 \text{ mA}$	-0.8		-2.0	V	
Drain-source ON resistance Forward transfer admittance		Provin	$V_{GS} = -4 \text{ V}, \text{ I}_D = -5 \text{ A}$		24	30		
		RDS (ON)	$V_{GS} = -10 \text{ V}, \text{ I}_{D} = -5 \text{ A}$	_	14	20	mΩ	
Forward transfer	orward transfer admittance		$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -5 \text{ A}$	9	19	_	S	
Input capacitance	9	C _{iss}		_	2260	_		
Reverse transfer capacitance		C _{rss}	V_{DS} = -10 V, V_{GS} = 0 V, f = 1 MHz	_	290	_	pF	
Reverse transfer capacitance Output capacitance		C _{oss}			350			
	Rise time	tr			5	_		
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $								
	Fall time	t _f	R = 3.0	_	34	_	- ns	
	Turn-OFF time	t _{off}		_	143	_		
		Qg	Van ~ -24 V. Van10 V.		45		nC	
Gate-source charge 1		Q _{gs1}			6.5			
		Q _{gd}]		10	_		

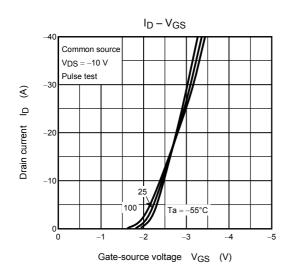
Source-Drain Ratings and Characteristics (Ta = 25°C)

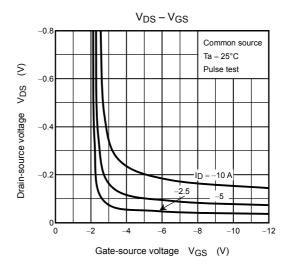
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Drain reverse current	Pulse	(Note 1)	I _{DRP}	—	_	_	-40	А
Forward voltage (diode)			V _{DSF}	$I_{DR} = -11 \text{ A}, \text{ V}_{GS} = 0 \text{ V}$			1.2	V

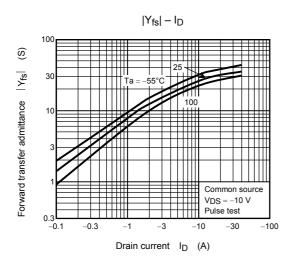
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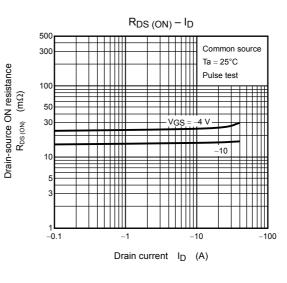




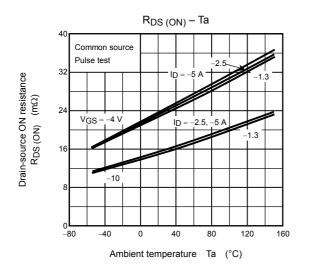


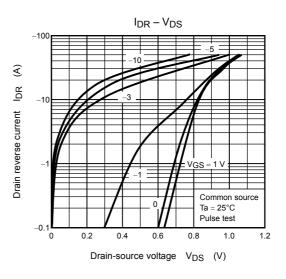


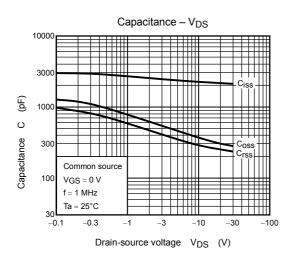


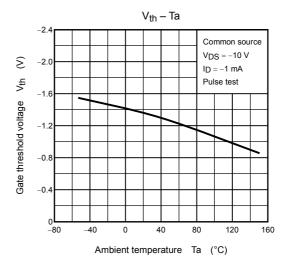


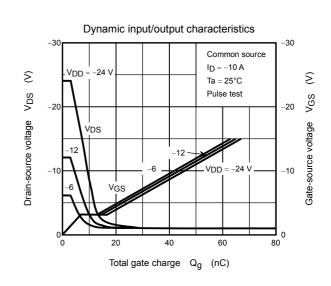
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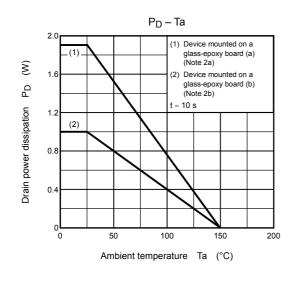


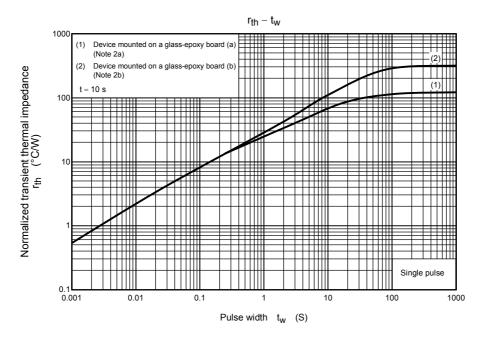




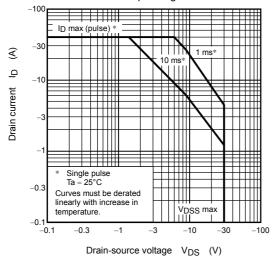








Safe operating area



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